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APPLICANTS Kazuyoshi Ueno, Tokyo, JAPAN;					
** CONTINUING DATA ***** This application is a DIV of 09/273,627 03/23/1999 PAT 6,670,270					
** FOREIGN APPLICATIONS ***** JAPAN 75195/1998 03/24/1998					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/19/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 4
Verified and Acknowledged Examiner's Signature _____ Initials <i>[Signature]</i>					
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TITLE Semiconductor device manufacturing apparatus and semiconductor device manufacturing method					
FILING FEE RECEIVED 870	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		